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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION

Applicant: Nakamura et al.

Art Unit: Unassigned

Serial No.: 10/709456

Examiner: Unassigned

Filed: May 6, 2004

Docket: JP920030017

Title: DYNAMIC SEMICONDUCTOR MEMORY DEVICE AND BIT LINE  
PRECHARGE METHOD THEREFOR.

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR 1.56, 1.97, 1.98**

Honorable Commissioner of Patents and Trademarks  
Washington, D. C. 20231

Sir:

Applicants submit herewith form PTO-1449, listing patents, publications, or other information of which they are aware which they believe may be material to patentability pursuant to 37 CFR 1.56(b), and in respect of which there may be a duty to disclose under 37 CFR 1.56(a), together with legible copies of the patents, publications, or other information listed. Any fees required in entering this response may be charged to Applicant's deposit account, 09-0456.

While the items submitted with this Information Disclosure Statement may be material to patentability pursuant to 37 CFR 1.56, in accordance with 37 CFR 1.97(h) it shall not be construed to be an admission that any patent, publication, or other information cited is "prior art" or is material to patentability for this invention unless specifically designated as such. In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other information material to patentability, as defined in 37 CFR 1.56(b), exists.

Respectfully submitted,

Date: 7/16/04

By:

Michael J. LaStrange, Reg. No. 53,207

Attorney for Applicants

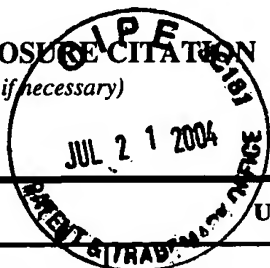
IBM Corporation

Intellectual Property Law - Mail Stop 972E

1000 River Street

Essex Junction, Vermont 05452

**INFORMATION DISCLOSURE CITATION**  
(Use several sheets if necessary)



Docket Number (Optional) <b>JP920030017</b>	Application Number <b>10/709456</b>
Applicant(s) <b>Nakamura et al.</b>	
Filing Date <b>05/06/04</b>	Group Art Unit <b>Unassigned</b>

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**U.S. PATENT APPLICATION PUBLICATIONS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	<b>JP 5-128858</b>	<b>05/25/93</b>	<b>Japan</b>				

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)


EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.